



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/727,595
Applicant : Gerhard Enders, et al.
Filed : December 5, 2003
TC/A.U. : 2811
Examiner : OWENS, Douglas W.
Confirmation No. : 7500
Docket No. : 0928.0006C
Customer No. : 27896
Title : Integrated Semiconductor Memory with a Selection
Transistor Formed at a Ridge (as amended)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL AMENDMENT

Sir:

Further in response to the Office Action mailed December 1, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims, which begins on page 18 of this paper.

Amendments to the Abstract begin on page 22 of this paper.

Amendments to the Drawings begin on page 23 of this paper.

Remarks begin on page 24 of this paper.